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<u>L14</u>	L13 and l8	25	<u>L14</u>
<u>L13</u>	l7 and pci	58	<u>L13</u>
<u>L12</u>	l7 and l4	22	<u>L12</u>
<u>L11</u>	l7 and l2	3	<u>L11</u>
<u>L10</u>	L8 and l7	26	<u>L10</u>
<u>L9</u>	L8 and l7	26	<u>L9</u>
<u>L8</u>	l2 or l4 or l5	20787	<u>L8</u>
<u>L7</u>	(north or south) near5 (bridge\$1 or gate\$1) near12 (rate\$1 or speed\$1 or clock\$1) near9 (cpu or central near1 processing or memory)	65	<u>L7</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L6</u>	(north or south) near5 (bridge\$1 or gate\$1) near12 (rate\$1 or speed\$1 or clock\$1) near9 (cpu or central near1 processing or memory)	63	<u>L6</u>
<u>L5</u>	(713/500-601 )![CCLS]	2456	<u>L5</u>
<u>L4</u>	(710/36-317 )![CCLS]	12410	<u>L4</u>
<u>L3</u>	(712/25-229)[CCLS]	6835	<u>L3</u>

L2

(712/25-229)! [CCLS]

L1

(north or south) near5 (bridge\$1 or gate\$1) near12 (rate\$1 or speed\$1 or clock\$1)

6835

L2

197

L1

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Solid-State Circuits, IEEE Journal of , Volume: 36 , Issue: 6 , June 2001

Pages:944 - 955

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☐ Check to search within this result set**Results Key:****JNL** = Journal or Magazine   **CNF** = Conference   **STD** = Standard**1 A design of a 10-W single-chip class D audio amplifier with very high efficiency using CMOS technology***Soo-Chang Choi; Jun-Woo Lee; Woo-Kang Jin; Jae-Hwan So; Suki Kim;*  
Consumer Electronics, IEEE Transactions on , Volume: 45 , Issue: 3 , Aug. 1999  
Pages:465 - 473[\[Abstract\]](#)   [\[PDF Full-Text \(628 KB\)\]](#)   **IEEE JNL****2 A study on the T5 fluorescent lamp ballast used contour vibration m piezoelectric transformer using a one-chip microcontroller***Hwang, L.H.; Yoo, J.H.; Kim, J.R.; Jang, E.S.; Lee, C.S.; Cho, M.T.; Kim, J.S.*  
Applications of Ferroelectrics, 2002. ISAF 2002. Proceedings of the 13th IEEE International Symposium on , 28 May-1 June 2002  
Pages:287 - 290[\[Abstract\]](#)   [\[PDF Full-Text \(403 KB\)\]](#)   **IEEE CNF****3 10-W single-chip class D power amplifier with very high efficiency f audio applications***Soo-Chang Choi; Jun-Woo Lee; Woo-Kang Jin; Suki Kim;*  
Consumer Electronics, 1999. ICCE. International Conference on , 22-24 June 1999  
Pages:18 - 19[\[Abstract\]](#)   [\[PDF Full-Text \(108 KB\)\]](#)   **IEEE CNF****4 Diagnosing single faults for interconnects in SRAM based FPGAs***Yinlei Yu; Jian Xu; Wei Kang Huang; Lombardi, F.;*  
Design Automation Conference, 1999. Proceedings of the ASP-DAC '99. Asia & South Pacific , 18-21 Jan. 1999  
Pages:283 - 286 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(368 KB\)\]](#) IEEE CNF

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**5 600 V single-chip power conversion system based on thin layer silicon-on-insulator**

*Letavic, T.; Arnold, E.; Simpson, M.; Peters, E.; Aquino, R.; Egloff, R.; Wong, Mukherjee, S.;*

SOI Conference, 1998. Proceedings., 1998 IEEE International , 5-8 Oct. 1998  
Pages:133 - 134

[\[Abstract\]](#) [\[PDF Full-Text \(276 KB\)\]](#) IEEE CNF

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**6 Fully digitalized high frequency link DC/AC converter**

*Sun, Y.S.; Kim, C.H.; Lee, J.W.; Kim, Y.H.; Yoo, Y.S.;*

Telecommunications Energy Conference, 1995. INTELEC '95., 17th Internatio  
, 29 Oct.-1 Nov. 1995  
Pages:659 - 663

[\[Abstract\]](#) [\[PDF Full-Text \(256 KB\)\]](#) IEEE CNF

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**7 A 7.1-GB/s low-power rendering engine in 2-D array-embedded me logic CMOS for portable multimedia system**

*Yong-Ha Park; Seon-Ho Han; Jung-Hwan Lee; Hoi-Jun Yoo;*

Solid-State Circuits, IEEE Journal of , Volume: 36 , Issue: 6 , June 2001  
Pages:944 - 955

[\[Abstract\]](#) [\[PDF Full-Text \(316 KB\)\]](#) IEEE JNL

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**8 A CMOS serial link for fully duplexed data communication**

*Kyeongho Lee; Sungjoon Kim; Gijung Ahn; Deog-Kyoon Jeong;*

Solid-State Circuits, IEEE Journal of , Volume: 30 , Issue: 4 , April 1995  
Pages:353 - 364

[\[Abstract\]](#) [\[PDF Full-Text \(864 KB\)\]](#) IEEE JNL

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**9 Dynamic modeling of a four-phase 8/6 switched reluctance motor u current and torque look-up tables**

*Chancharoensook, P.; Rahman, M.F.;*

IECON 02 [Industrial Electronics Society, IEEE 2002 28th Annual Conference the] , Volume: 1 , 5-8 Nov. 2002  
Pages:491 - 496 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(458 KB\)\]](#) IEEE CNF

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**10 A simple pulsed CO<sub>2</sub> laser with long pulse duration of millisecond o**

*Hyun-Ju-Chung; Dong-Hoon Lee; Jong-Han Joung; Sung-Joon Park; Yu-Soo I Hee-Je Kim;*

Industrial Electronics, 2001. Proceedings. ISIE 2001. IEEE International Symposium on , Volume: 2 , 12-16 June 2001  
Pages:952 - 955 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(256 KB\)\]](#) IEEE CNF

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**11 Embedded tutorial: issues on SOC testing in DSM era**

*Aikyo, T.;*

Design Automation Conference, 2000. Proceedings of the ASP-DAC 2000. Asi:

South Pacific , 25-28 Jan. 2000

Pages:515

[\[Abstract\]](#) [\[PDF Full-Text \(32 KB\)\]](#) IEEE CNF

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**12 High performance DSP/FPGA controller for implementation of computationally intensive algorithms**

*Bester, D.D.; du Toit, J.A.; Enslin, J.H.R.;*

Industrial Electronics, 1998. Proceedings. ISIE '98. IEEE International Symposium on , Volume: 1 , 7-10 July 1998

Pages:240 - 244 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(516 KB\)\]](#) IEEE CNF

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**13 Negative sequence current cancellation with DSP and space vector controlled PWM modulated inverter**

*Malengret, M.; Farquharson, A.; Enslin, J.H.R.;*

Communications and Signal Processing, 1998. COMSIG '98. Proceedings of the 1998 South African Symposium on , 7-8 Sept. 1998

Pages:237 - 242

[\[Abstract\]](#) [\[PDF Full-Text \(352 KB\)\]](#) IEEE CNF

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**14 Dead time compensation of current controlled inverter using space vector modulation method**

*Won Seok Oh; Yong Tae Kim; Hee Jun Kim;*

Power Electronics and Drive Systems, 1995., Proceedings of 1995 International Conference on , 21-24 Feb. 1995

Pages:374 - 378 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(232 KB\)\]](#) IEEE CNF

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---

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L14: Entry 1 of 25

File: PGPB

Dec 4, 2003

PGPUB-DOCUMENT-NUMBER: 20030225955

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030225955 A1

TITLE: Data modem

PUBLICATION-DATE: December 4, 2003

## INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Feldstein, Andy A.	Potomac	MD	US	
Woodward, Robert A.	Clifton	VA	US	
Smith, Gregory N.	Herndon	VA	US	
Weinberger, Barry P.	Herndon	VA	US	
Smith, Troy	Ashburn	VA	US	
Long, Shawn	Fairfax	VA	US	
Wexler, Michael M.	Washington	DC	US	

US-CL-CURRENT: 710/306

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWC	Draw. Da
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☐ 2. Document ID: US 20030208654 A1

L14: Entry 2 of 25

File: PGPB

Nov 6, 2003

PGPUB-DOCUMENT-NUMBER: 20030208654

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030208654 A1

TITLE: Computer system architecture with hot pluggable main memory boards

PUBLICATION-DATE: November 6, 2003

## INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
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h e b b g e e e f e ef b e

Krontz, Jeoff M.	Tomball	TX	US
Depew, Kevin G.	Kingwood	TX	US
Nguyen, John D.	Cypress	TX	US
Heinrich, David F.	Tomball	TX	US
Engler, David W.	Cypress	TX	US
Nguyen, Vincent	Sugarland	TX	US
Dow, Randolph O.	Waller	TX	US
Kidwai, Owais	Cypress	TX	US

US-CL-CURRENT: 710/312

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 3. Document ID: US 20030182489 A1

L14: Entry 3 of 25

File: PGPB

Sep 25, 2003

PGPUB-DOCUMENT-NUMBER: 20030182489  
PGPUB-FILING-TYPE: new  
DOCUMENT-IDENTIFIER: US 20030182489 A1

TITLE: Method and apparatus for generating traffic in an electronic bridge via a local controller

PUBLICATION-DATE: September 25, 2003

## INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Wang, Jennifer C.	Tempe	AZ	US	
Joshi, Aniruddha P.	Chandler	AZ	US	
Munguia, Peter R.	Chandler	AZ	US	

US-CL-CURRENT: 710/306

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 4. Document ID: US 20030074515 A1

L14: Entry 4 of 25

File: PGPB

Apr 17, 2003

PGPUB-DOCUMENT-NUMBER: 20030074515  
PGPUB-FILING-TYPE: new  
DOCUMENT-IDENTIFIER: US 20030074515 A1

TITLE: System for supporting both serial and parallel storage devices on a connector

PUBLICATION-DATE: April 17, 2003

## INVENTOR-INFORMATION:

h e b b g e e e f e ef b e



NAME	CITY	STATE	COUNTRY	RULE-47
Resnick, Russell Alan	Raleigh	NC	US	

US-CL-CURRENT: 710/313; 710/316

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Drawn De
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☐ 5. Document ID: US 20030056051 A1

L14: Entry 5 of 25

File: PGPB

Mar 20, 2003

PGPUB-DOCUMENT-NUMBER: 20030056051  
PGPUB-FILING-TYPE: new  
DOCUMENT-IDENTIFIER: US 20030056051 A1

TITLE: System and method for connecting a universal serial bus device to a host computer system

PUBLICATION-DATE: March 20, 2003

## INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Burke, Thomas Charles	Durham	NC	US	
Cromer, Daryl Carvis	Cary	NC	US	
Dayan, Richard Alan	Wake Forest	NC	US	
Kern, Eric	Durham	NC	US	
Springfield, Randall Scottt	Chapel Hill	NC	US	

US-CL-CURRENT: 710/302

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Drawn De
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☐ 6. Document ID: US 20020147868 A1

L14: Entry 6 of 25

File: PGPB

Oct 10, 2002

PGPUB-DOCUMENT-NUMBER: 20020147868  
PGPUB-FILING-TYPE: new  
DOCUMENT-IDENTIFIER: US 20020147868 A1

TITLE: Method and system for dividing configuration space

PUBLICATION-DATE: October 10, 2002

## INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Tsai, Chau-Chad	Taipei		TW	

US-CL-CURRENT: 710/104

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 7. Document ID: US 20020120803 A1

L14: Entry 7 of 25

File: PGPB

Aug 29, 2002

PGPUB-DOCUMENT-NUMBER: 20020120803  
PGPUB-FILING-TYPE: new  
DOCUMENT-IDENTIFIER: US 20020120803 A1

TITLE: Link bus for a hub based computer architecture

PUBLICATION-DATE: August 29, 2002

## INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Porterfield, A. Kent	New Brighton	MN	US	

US-CL-CURRENT: 710/305

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 8. Document ID: US 20020023190 A1

L14: Entry 8 of 25

File: PGPB

Feb 21, 2002

PGPUB-DOCUMENT-NUMBER: 20020023190  
PGPUB-FILING-TYPE: new  
DOCUMENT-IDENTIFIER: US 20020023190 A1

TITLE: Framework with multiple selections for south bridge and north bridge connecting

PUBLICATION-DATE: February 21, 2002

## INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Peng, Sheng-Chang	Taipei		TW	

US-CL-CURRENT: 710/308; 710/309

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 9. Document ID: US 20020019953 A1

L14: Entry 9 of 25

File: PGPB

Feb 14, 2002

PGPUB-DOCUMENT-NUMBER: 20020019953  
PGPUB-FILING-TYPE: new  
DOCUMENT-IDENTIFIER: US 20020019953 A1

TITLE: Clock supply control apparatus and method

PUBLICATION-DATE: February 14, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Urita, Kenji	Kawasaki		JP	

US-CL-CURRENT: 713/600

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 10. Document ID: US 6618782 B1

L14: Entry 10 of 25

File: USPT

Sep 9, 2003

US-PAT-NO: 6618782

DOCUMENT-IDENTIFIER: US 6618782 B1

TITLE: Computer interconnection bus link layer

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 11. Document ID: US 6615345 B1

L14: Entry 11 of 25

File: USPT

Sep 2, 2003

US-PAT-NO: 6615345

DOCUMENT-IDENTIFIER: US 6615345 B1

TITLE: System and method for regulating data capture in response to data strobe using preamble, postamble and strobe signature

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 12. Document ID: US 6499079 B1

L14: Entry 12 of 25

File: USPT

Dec 24, 2002

US-PAT-NO: 6499079

DOCUMENT-IDENTIFIER: US 6499079 B1

TITLE: Subordinate bridge structure for a point-to-point computer interconnection bus

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 13. Document ID: US 6496361 B2

L14: Entry 13 of 25

File: USPT

Dec 17, 2002

US-PAT-NO: 6496361

DOCUMENT-IDENTIFIER: US 6496361 B2

TITLE: Embedded CMOS camera in a laptop computer

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw D
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☐ 14. Document ID: US 6470410 B1

L14: Entry 14 of 25

File: USPT

Oct 22, 2002

US-PAT-NO: 6470410

DOCUMENT-IDENTIFIER: US 6470410 B1

TITLE: Target side concentrator mechanism for connecting multiple logical pipes to a single function utilizing a computer interconnection bus

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw D
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☐ 15. Document ID: US 6457084 B1

L14: Entry 15 of 25

File: USPT

Sep 24, 2002

US-PAT-NO: 6457084

DOCUMENT-IDENTIFIER: US 6457084 B1

TITLE: Target side distributor mechanism for connecting multiple functions to a single logical pipe of a computer interconnection bus

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw D
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☐ 16. Document ID: US 6457081 B1

L14: Entry 16 of 25

File: USPT

Sep 24, 2002

US-PAT-NO: 6457081

DOCUMENT-IDENTIFIER: US 6457081 B1

TITLE: Packet protocol for reading an indeterminate number of data bytes across a computer interconnection bus

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw D
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☐ 17. Document ID: US 6421751 B1

L14: Entry 17 of 25

File: USPT

Jul 16, 2002

US-PAT-NO: 6421751

DOCUMENT-IDENTIFIER: US 6421751 B1

TITLE: Detecting a no-tags-free condition in a computer system having multiple outstanding transactions

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWC	Draw D
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☐ 18. Document ID: US 6336179 B1

L14: Entry 18 of 25

File: USPT

Jan 1, 2002

US-PAT-NO: 6336179

DOCUMENT-IDENTIFIER: US 6336179 B1

TITLE: Dynamic scheduling mechanism for an asynchronous/isochronous integrated circuit interconnect bus

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWC	Draw D
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☐ 19. Document ID: US 6314522 B1

L14: Entry 19 of 25

File: USPT

Nov 6, 2001

US-PAT-NO: 6314522

DOCUMENT-IDENTIFIER: US 6314522 B1

TITLE: Multi-voltage level CPU module

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWC	Draw D
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☐ 20. Document ID: US 6253304 B1

L14: Entry 20 of 25

File: USPT

Jun 26, 2001

US-PAT-NO: 6253304

DOCUMENT-IDENTIFIER: US 6253304 B1

TITLE: Collation of interrupt control devices

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWC	Draw D
------	-------	----------	-------	--------	----------------	------	-----------	-----------	-------------	--------	-----	--------

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Next Page

Go to Doc#

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☐ 21. Document ID: US 6241400 B1

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L14: Entry 21 of 25

File: USPT

Jun 5, 2001

US-PAT-NO: 6241400

DOCUMENT-IDENTIFIER: US 6241400 B1

TITLE: Configuration logic within a PCI compliant bus interface unit which can be selectively disconnected from a clocking source to conserve power

DATE-ISSUED: June 5, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Melo; Maria L.	Houston	TX		
Alzien; Khaldoun	Houston	TX		

US-CL-CURRENT: 710/313

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
------	-------	----------	-------	--------	----------------	------	-----------	-----------	-------------	--------	------	----------

☐ 22. Document ID: US 6151651 A

L14: Entry 22 of 25

File: USPT

Nov 21, 2000

US-PAT-NO: 6151651

DOCUMENT-IDENTIFIER: US 6151651 A

TITLE: Communication link with isochronous and asynchronous priority modes coupling bridge circuits in a computer system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 23. Document ID: US 6094700 A

L14: Entry 23 of 25

File: USPT

Jul 25, 2000

US-PAT-NO: 6094700

DOCUMENT-IDENTIFIER: US 6094700 A

TITLE: Serial bus system for sending multiple frames of unique data

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw D
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☐ 24. Document ID: US 6065122 A

L14: Entry 24 of 25

File: USPT

May 16, 2000

US-PAT-NO: 6065122

DOCUMENT-IDENTIFIER: US 6065122 A

TITLE: Smart battery power management in a computer system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw D
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☐ 25. Document ID: US 5857085 A

L14: Entry 25 of 25

File: USPT

Jan 5, 1999

US-PAT-NO: 5857085

DOCUMENT-IDENTIFIER: US 5857085 A

TITLE: Interface device for XT/AT system devices on high speed local bus

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw D
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Term	Documents
(13 AND 8).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	25
(L13 AND L8).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	25

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